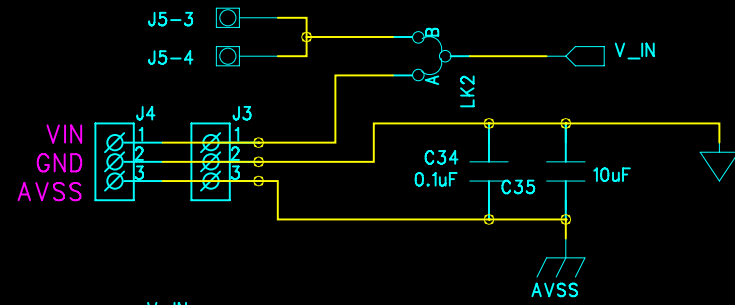


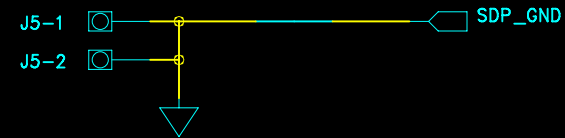
GPIO Ref to 5V Analog
Provision for voltage divider
for low voltages



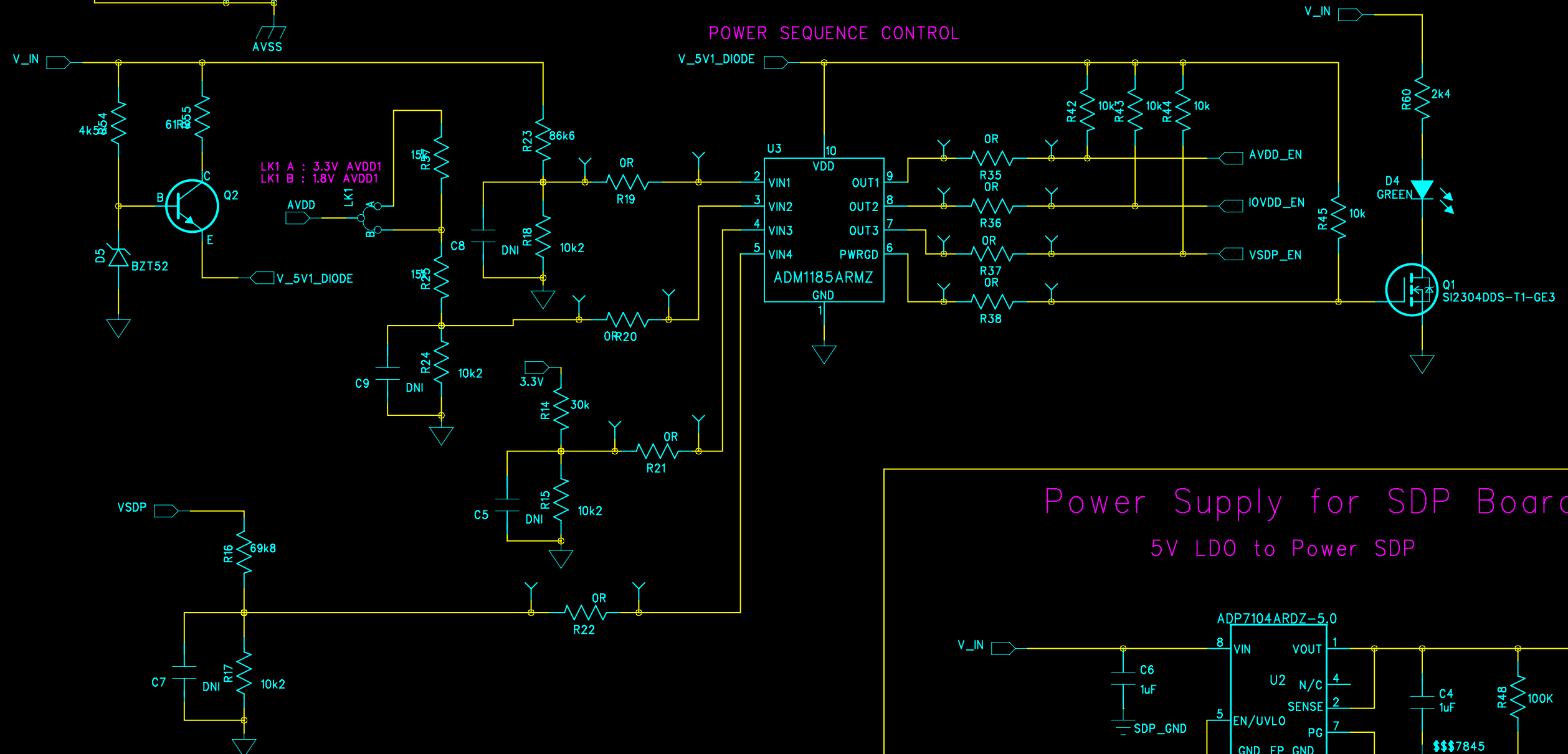
POWER SUPPLY



Ground Star point. Place near to V_in jack plug J5

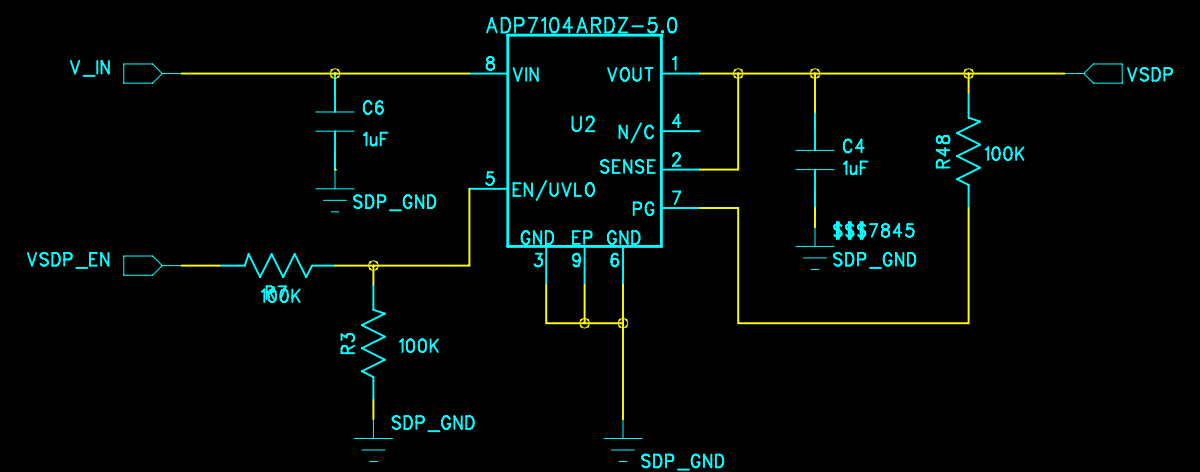


POWER SEQUENCE CONTROL

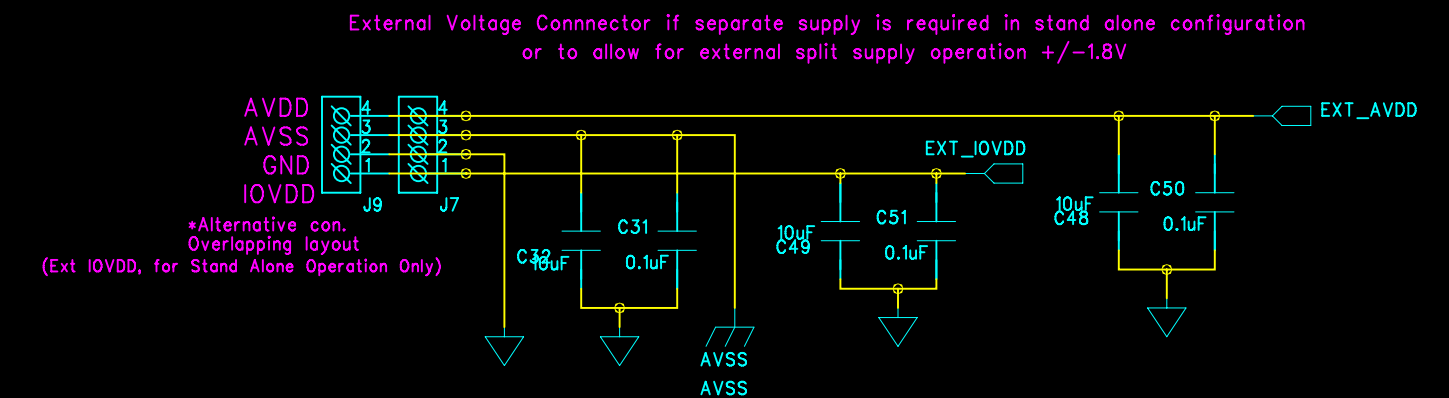
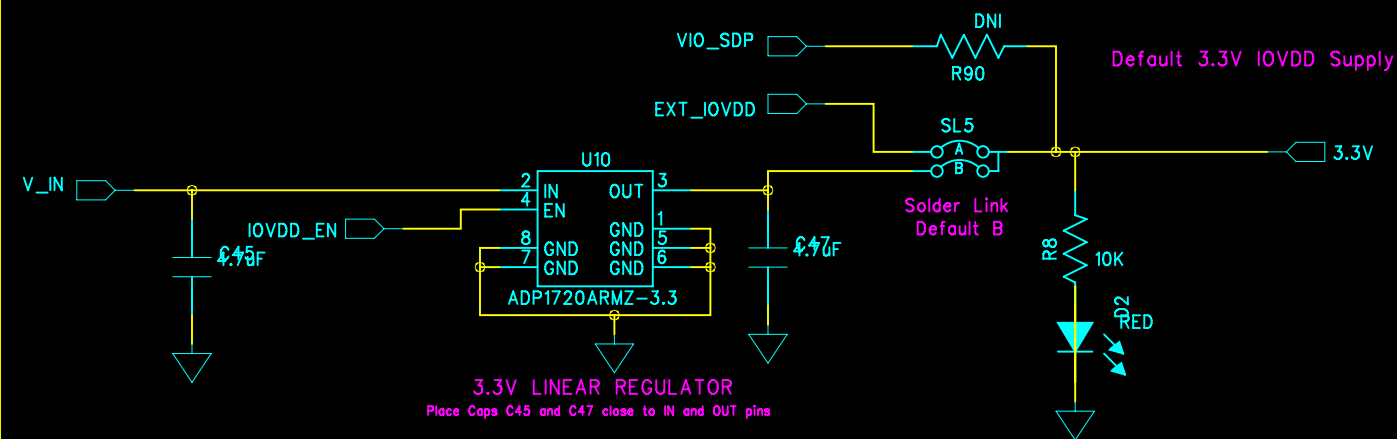
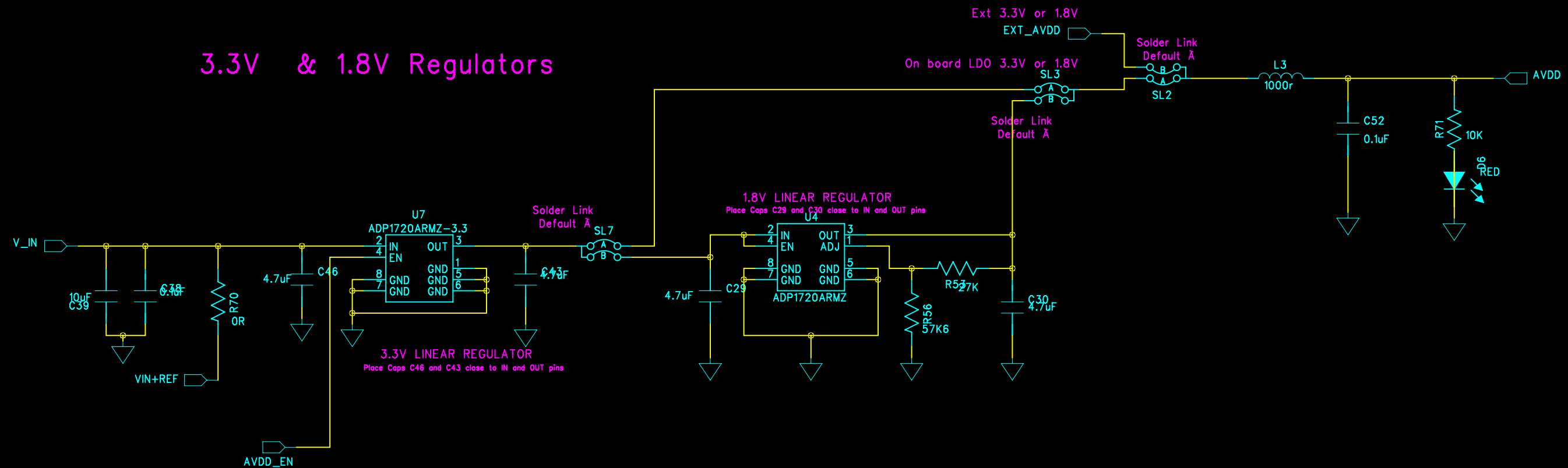


C160-C163 placed only if specific delays needed

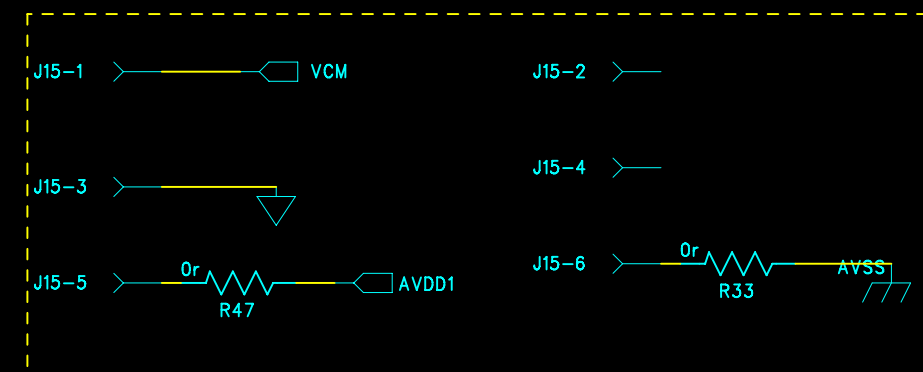
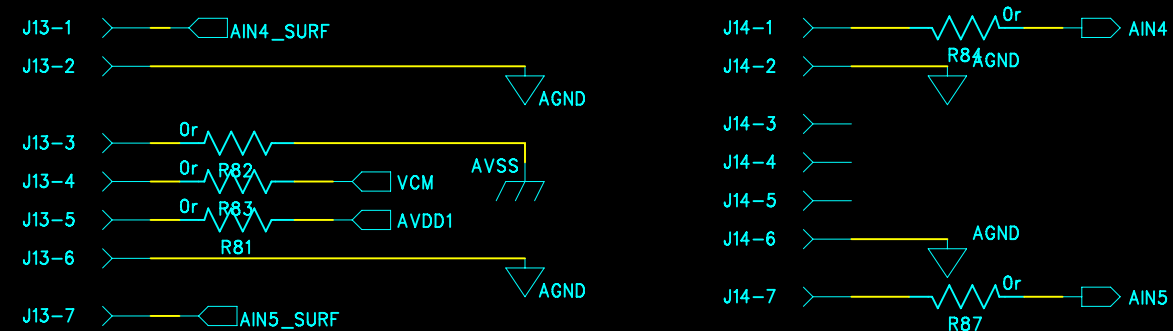
Power Supply for SDP Board 5V LDO to Power SDP



3.3V & 1.8V Regulators



OPTIONAL HEADER CONNECTOR

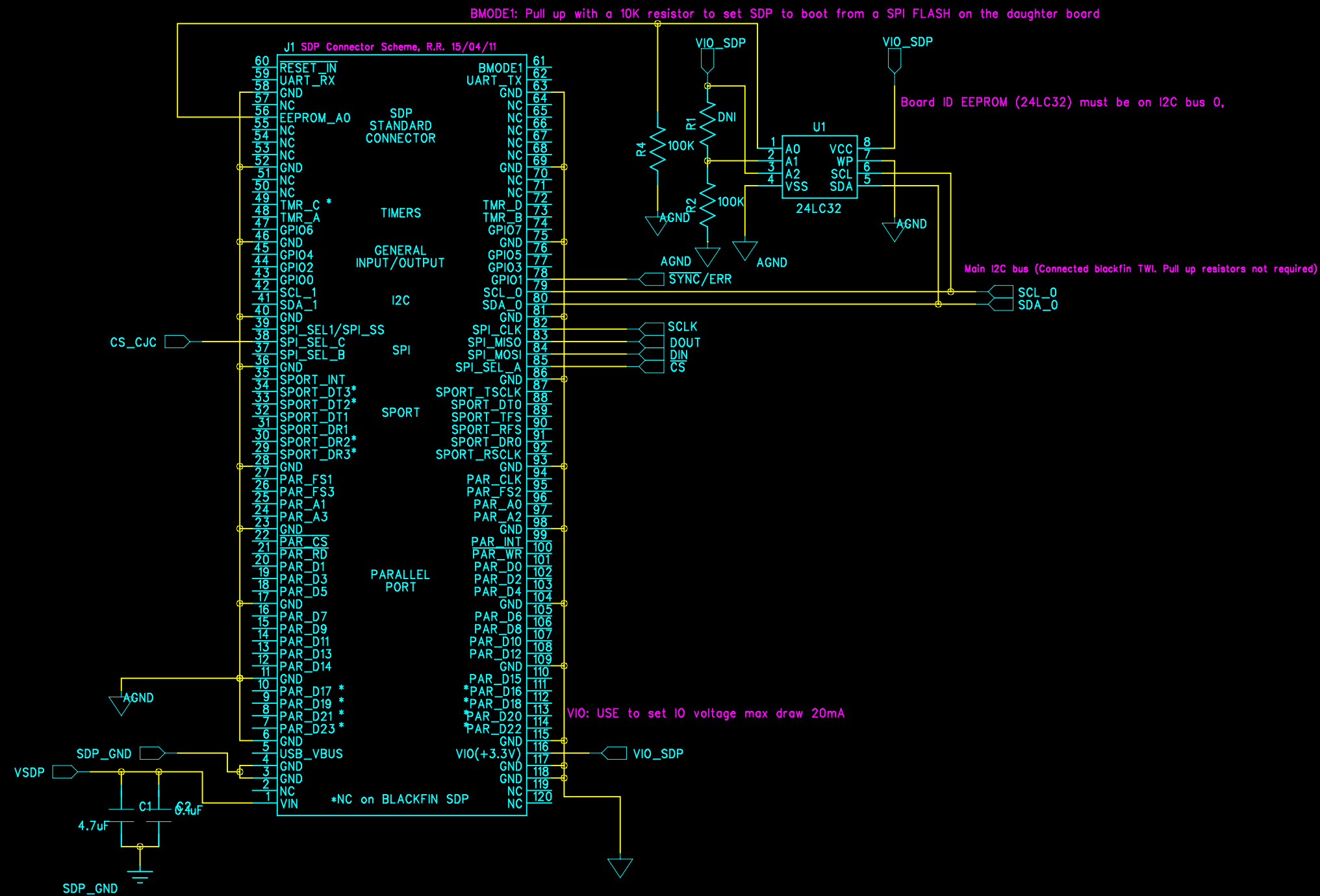


SDP CONNECTOR EEPROM-SW/USB ID

VIO: USE to set IO voltage max draw 20mA

VIN: Use this pin to power the SDP requires 4-7V 200mA

BMODE1: Pull up with a 10K resistor to set SDP to boot from a SPI FLASH on the daughter board



VIN: Use this pin to power the SDP requires 5V 200mA

I2C bus 1 is common across both connectors on SDP - Pull up resistors required (connected to blackfin GPIO - use I2C_0 first)